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Certificate Number : ICCN0325

Date of issuance 19 Jun 2026

Expiry Date: 19 Jun 2027

Company: STMicroelectronics (Rousset) SAS

Address: ZI de Rousset
Avenue Coq
Rousset Cedex 01 13106 France

Master Component: ST31S600

Hardware Revision: rev. A

Child Lot 1: ST31S500

Child Lot 2: ST31S400

Child Lot 3: -

Child Lot 4: -

Child Lot 5: -

Manufacturing site(s): STMicroelectronics Crolles (France)

Firmware name / version: ST31S_FWv4 v4.0.3

Crypto. library name / version: -

Other libraries name / version: Optional RngLib v3.0.0

Bootloader name / version: included in Firmware

Security Laboratory: Thales

Conditions of Certification: User Guidance document(s) must be followed.

Authorized by: Alan Mushing
Security Evaluation Working Group Chair
EMVCo, LLC
Date: 01 Jul 2026

Disclaimer: Although the secure implementation of any security mechanisms and product functionalities may be evaluated, the EMVCo Security Evaluation Process does not validate the cryptographic algorithms, methods and protocols themselves nor the absence of flaws or defects in the specifications used for product development.

The ICCN number must be mentioned to all vendors or when shipping the product. The use of the ICCN number is limited to the product as detailed below. Please also reference the ICCN number in any communication with EMVCo.

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User Guidance Documents:

- ST31S platform (ST31S600, ST31S500, ST31S400) – Secure dual interface microcontroller with enhanced security and up to 608 Kbytes of flash memory – Datasheet, DS_ST31S, v1
- ST31S platform firmware V4 – User manual UM_ST31S_FWv4, v1
- Security guidance of the ST31S600 secure MCU subsystem – Application note, AN_SECU_ST31S, v1
- Random number generation for ST31S – User manual, UM_ST31S_TRNG, v1
- Random number generator library RngLib 3.0.x – User manual, UM_RngLib_3.0, v1
- RngLib 3.0.0 for ST31S platform – Release note, RN_ST31S_RngLib_3.0.0, v1

Certification Reference Documents:

- EMVCo Security Evaluation Process, v5.5, January 2026
- Security Guidelines for Smart Card Integrated Circuits, v2.2, December 2022

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