



## EMVCo Product Approval (IC)

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The ICCN number must be mentioned to all vendors or when shipping the product.  
The use of the ICCN number is limited to the product as detailed below.  
Please also reference the ICCN number in any communication with EMVCo.

**ICCN:** ICCN0271

**Date ICCN issued:** 03 Apr 2020

**ICCN Expiry Date:** 03 Apr 2022

**Company:** Infineon Technologies AG

**Master Component:** IFX\_ECI\_2Fh[9h-Ch]

**Hardware Revision:** T11

Child Lot 1: IFX\_ECI\_49h[1h-1Ah]

Child Lot 2: IFX\_ECI\_4Ah[1h-1Ch]

Child Lot 3: IFX\_ECI\_51h[1h-3h]

Child Lot 4: IFX\_ECI\_64h[1h-3h]

Child Lot 5: -

Manufacturing site(s): GlobalFoundries, Singapore

Firmware name / version: FL 09.10.0007 /09.12.0005, PFL 09.10.90.9, see Bootloader

Crypto. library name / version: SCL 2.11.003 & 2.15.000, ACL 3.02.000 & 3.33.003

Other libraries name / version: NRG 05.03.4097, HSL 3.52.9708, HCL 1.13.002, UMSLC 01.30.0564, RCL 1.10.007

Bootloader name / version: BOS & POWS 80.306.15.0, 80.306.16.0 & 80.306.16.1

Security Laboratory: TUVIT

**User Guidance:**

- 32-bit Security Controller – V11, Hardware Reference Manual, V6.2, 21 Dec 2020
- SLx1/SLx3(40 nm) Security Controllers, Programmer's Reference Manual, V4.6, 13 Oct 2020
- 32-bit Security Controller – V11, Security Guidelines, v1.00-2661, 5 Oct 2020
- Production and personalization 32-bit ARM-based security controller, v09.10, 29 Nov 2019 & v09.12, 3 Jul 2020
- 32-bit Security Controller Crypto2304T V3, User Manual, V2.0, 24 Apr 2019
- HSL SLCx7 40nm with ROM core (optional), v3.52.9708, 25 Jan 2021
- UMSLC library for SLCx7 in 40nm, v01.30.0564, 19 Jun 2019
- SCL37-SCP-v440-C40 Symmetric Crypto Library for SCP-v440 AES/DES/MAC (optional), v2.11.003, 16 Jun 2021
- SCL37-SCP-v440-C40 Symmetric Crypto Library for SCP-v440 AES/DES/MAC (optional), v2.15.000, 1 Jun 2021
- ACL37-Crypto2304T-C40 Asymmetric Crypto Library for Crypto2304T RSA/ECC/Toolbox (optional), v3.02.000, 7 Feb 2020
- ACL37-Crypto2304T-C40 Asymmetric Crypto Library for Crypto2304T RSA/ECC/Toolbox, v3.33.003, 18 Mar 2021
- RCL37-X-C40 Random Crypto Library for SCP-v440 & RNG-v3 DRBG/HWRNG, User interface manual, v1.10.007, 16 Jun 2020
- HCL37-CPU-C40 Hash Crypto Library for CPU SHA, User interface manual, v1.13.002, 7 May 2020
- Performance Flash Loader, Supplement to Flash Loader V9, Application Note, SLx1/SLx3 (40 nm) family, v1.8, 14 May 2020

**Conditions of Certification:** Guidance document(s) must be followed.

Disclaimer: Although the secure implementation of any security mechanisms and product functionalities may be evaluated, the EMVCo Security Evaluation Process does not validate the cryptographic algorithms, methods and protocols themselves nor the absence of flaws or defects in the specifications used for product development.

## **Please accept this document as confirmation of the EMVCo Security Evaluation process**

The EMVCo Security Evaluation Process is intended to provide valuable and practical information relating to the general security performance characteristics and the suitability of use for smart card related products and IC chip-based tokens. The EMVCo Security Evaluation Process is designed to ensure a robust security foundation for these products at the product family and component level. The EMVCo Security Evaluation Process is an evolving process in relation to new attack techniques and technology. EMVCo therefore reserves the right to perform new/random security testing throughout the lifetime of the card which may impact certification. The full terms and conditions upon which EMVCo Compliance Certificates are issued by EMVCo are contained in the EMVCo Security Evaluation Process Document and the EMVCo Security Evaluation Certification Contract.