



## EMVCo Product Approval (IC)

Please accept this document as confirmation of the EMVCo Security Evaluation process

The ICCN number must be mentioned to all vendors or when shipping the product.

The use of the ICCN number is limited to the product as detailed below.

Please also reference the ICCN number in any communication with EMVCo.

**ICCN: ICCN0241**

**Date ICCN issued: 30 Jan 2017**

**ICCN Expiry Date: 30 Jan 2022**

**Company: Infineon Technologies AG**

**Master Component: IFX\_ECI\_18h[1h]**

**Hardware Revision: G13**

Child Lot 1: IFX\_ECI\_1Ah[1h-5h]

Child Lot 2: IFX\_ECI\_1Bh[1h-19h]

Child Lot 3: IFX\_ECI\_1Ch[1h-6h]

Child Lot 4: IFX\_ECI\_33h[1h]

Child Lot 5: -

Manufacturing site(s): TSMC Tainan (Taiwan)

Firmware name / version: v80.101.07.0, v80.101.07.1 or v80.101.07.2

Crypto. library name / version: ACL v2.06.003, v2.07.003 & v2.08.007, SCL v2.02.010/v02.04.002

Other libraries name / version: HSL 01.22.4346, v02.01.6634, v03.11.8339 & v03.12.8812, CCL 2.00.0004

Bootloader name / version: Included in FW identifier

Security Laboratory: T-Systems

**User Guidance:**

- 16-bit Security Controller Family - V02, Hardware Reference Manual (HRM), Rev. 5.0, 13 Jun 2019
- 16-bit Security Controller in 65nm, Production and Personalization, User's Manual, Rev. 3.6, 24 Jun 2019
- 16-bit Security Controller, 65-nm Technology, Programmer's Reference Manual (PRM), Rev. 9.14, 3 Dec 2019
- 16-bit Security Controller, Crypto@2304T V3, User Manual (CUM), Rev. 1.4.1, 10 Nov 2014
- 16-bit Security Controller - V02, Security Guidelines, Rev. 1.01-2479, 9 Mar 2020
- 16-bit Security Controller - V02, Errata Sheet, Rev. 7.1, 5 Nov 2019
- Hardware Support Library (HSL), Rev. 01.22.4346
- Hardware Support Library (HSL), Rev. 02.01.6634
- Hardware Support Library for SLCx2 (HSL), Rev. 03.11.8339
- Hardware Support Library for SLCx2 (HSL), Rev. 03.12.8812
- CL52 Asymmetric Crypto Library for Crypto@2304T RSA/ECC/Tbx User Interface, Rev. 2.06.003, 25 Apr 2019
- CL52 Asymmetric Crypto Library for Crypto@2304T RSA/ECC/Tbx User Interface, Rev. 2.07.003, 25 Apr 2019
- CL52 Asymmetric Crypto Library for Crypto@2304T RSA/ECC/Tbx User Interface, Rev. 2.08.007, 25 Apr 2019
- SCL52 Symmetric Cryptographic Library for DES / AES, User Interface, v2.02.010, 9 Dec 2016
- SCL52-SCP-v4-C65 Symmetric Crypto Library for SCP-v4 DES / AES, User Interface, v2.04.002, 15 Jan 2018
- CIPURSE™ Crypto Library, CCLX2xCIP v02.00.0004, CIPURSE™ V2, Compliant to OSPT™ Alliance CIPURSE™ V2 Cryptographic Protocol, User Interface, Rev. 1.6, 2 Feb 2018

**Conditions of Certification:** Guidance document(s) must be followed.

**Disclaimer:** Although the secure implementation of any security mechanisms and product functionalities may be evaluated, the EMVCo Security Evaluation Process does not validate the cryptographic algorithms, methods and protocols themselves nor the absence of flaws or defects in the specifications used for product development.

The EMVCo Security Evaluation Process is intended to provide valuable and practical information relating to the general security performance characteristics and the suitability of use for smart card related products and IC chip-based tokens. The EMVCo Security Evaluation Process is designed to ensure a robust security foundation for these products at the product family and component level. The EMVCo Security Evaluation Process is an evolving process in relation to new attack techniques and technology. EMVCo therefore reserves the right to perform new/random security testing throughout the lifetime of the card which may impact certification. The full terms and conditions upon which EMVCo Compliance Certificates are issued by EMVCo are contained in the EMVCo Security Evaluation Process Document and the EMVCo Security Evaluation Certification Contract.