



EMVCo Product Approval (IC)

Please accept this document as confirmation of the EMVCo Security Evaluation process

The ICCN number must be mentioned to all vendors or when shipping the product.
The use of the ICCN number is limited to the product as detailed below.
Please also reference the ICCN number in any communication with EMVCo.

ICCN: ICCN0237

Date ICCN issued: 09 Dec 2016

ICCN Expiry Date: 09 Dec 2021

Company: Infineon Technologies AG

Master Component: IFX_ECI_Fh[1h-4h]

Hardware Revision: H13

Child Lot 1: IFX_ECI_12h[1h-5Ch]

Child Lot 2: IFX_ECI_13h[1h-5Ah]

Child Lot 3: IFX_ECI_1Eh[1h-5h], IFX_ECI_2Ah[1h-Fh]

Child Lot 4: IFX_ECI_2Bh[1h-3h], IFX_ECI_31h[1h-3Bh]

Child Lot 5: IFX_ECI_34h[1h-4h] and IFX_ECI_4Dh[1h-2h]

Manufacturing site(s): TSMC Fab14 Tainan (Taiwan) and TSMC Fab12 Hsinchu (Taiwan)

Firmware name / version: FW Identifier v80.100.17.0, v80.100.17.1, 80.100.17.2 & 80.100.17.3

Crypto. library name / version: ACL2.06.003/2.07.003/2.05.908/2.08.007 SCL2.02.010/2.04.002 CL2.00.0004

Other libraries name / version: HSL1.22.4346/2.01.6634/3.11.8339/3.12.8812 NRG Lib02.03.3446/02.04.3957

Bootloader name / version: included in FW identifier

Security Laboratory: T-Systems, TUVIT (since Feb. 2019 update)

User Guidance:

- 16-bit Security Controller Family -V01, Hardware Reference Manual (HRM), Rev. 7.0, 11 Jun 2019
- Production and Personalization, 16-bit Security Controller in 65nm, Rev. 3.6, 24 Jun 2019
- 16-bit Security Controller, 65-nm Technology, Programmer's Reference Manual (PRM), Rev. 9.14, 3 Dec 2019
- 16-bit Security Controller, Crypto@2304T V3, User Manual (CUM), Rev. 1.4.1, 10 Nov 2014
- 16-bit Security Controller - V01, Security Guidelines (SG), Rev. 1.01-2596, 20 Aug 2020
- 16-bit Security Controller - V01, Errata Sheet, Rev. 11.0, 26 Nov 2019
- CL52 Asymmetric Crypto Library for Crypto@2304T RSA/ECC/Toolbox, Rev. 2.06.003, 30 Nov 2020, Rev. 2.07.003, 9 Dec 2020 Rev. 2.08.007, 30 Nov 2020, Rev.2.05.908, 27 Jun 2017
- CIPURSE™ Crypto Library, CCLX2xCIP v02.00.0004, CIPURSE™ V2, Compliant to OSPT™ Alliance CIPURSE™ V2 Cryptographic Protocol, User Interface, Rev. 1.6, 2 Feb 2018
- Hardware Support Library (HSL) SLCx2, User Guidance, Rev. 01.22.4346, 2016, Rev. 02.01.6634, 2017, Rev. 03.11.8339, 2018, Rev. 03.12.8812, Rev. 1.1, 8 Jul 2019
- SLC52 Symmetric Crypto Library for SCPv4 DES/AES, User Interface, v2.02.010, 09 Dec 2016
- SCL52 Symmetric Crypto Library for SCPv4 DES/AES, User Interface, v2.04.002, 22 May 2018

Conditions of Certification: Guidance document(s) must be followed.

Disclaimer: Although the secure implementation of any security mechanisms and product functionalities may be evaluated, the EMVCo Security Evaluation Process does not validate the cryptographic algorithms, methods and protocols themselves nor the absence of flaws or defects in the specifications used for product development.

The EMVCo Security Evaluation Process is intended to provide valuable and practical information relating to the general security performance characteristics and the suitability of use for smart card related products and IC chip-based tokens. The EMVCo Security Evaluation Process is designed to ensure a robust security foundation for these products at the product family and component level. The EMVCo Security Evaluation Process is an evolving process in relation to new attack techniques and technology. EMVCo therefore reserves the right to perform new/random security testing throughout the lifetime of the card which may impact certification. The full terms and conditions upon which EMVCo Compliance Certificates are issued by EMVCo are contained in the EMVCo Security Evaluation Process Document and the EMVCo Security Evaluation Certification Contract.